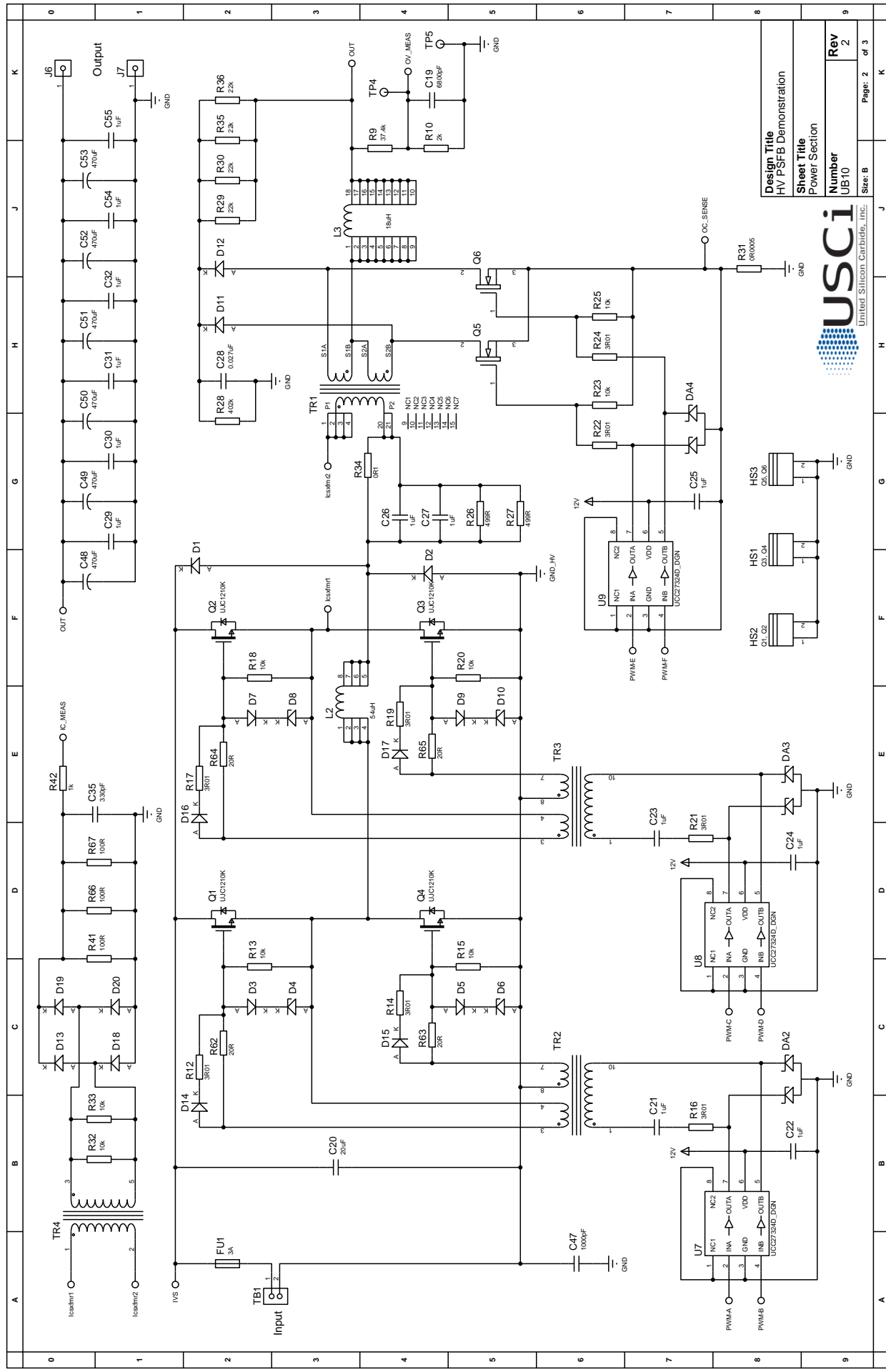


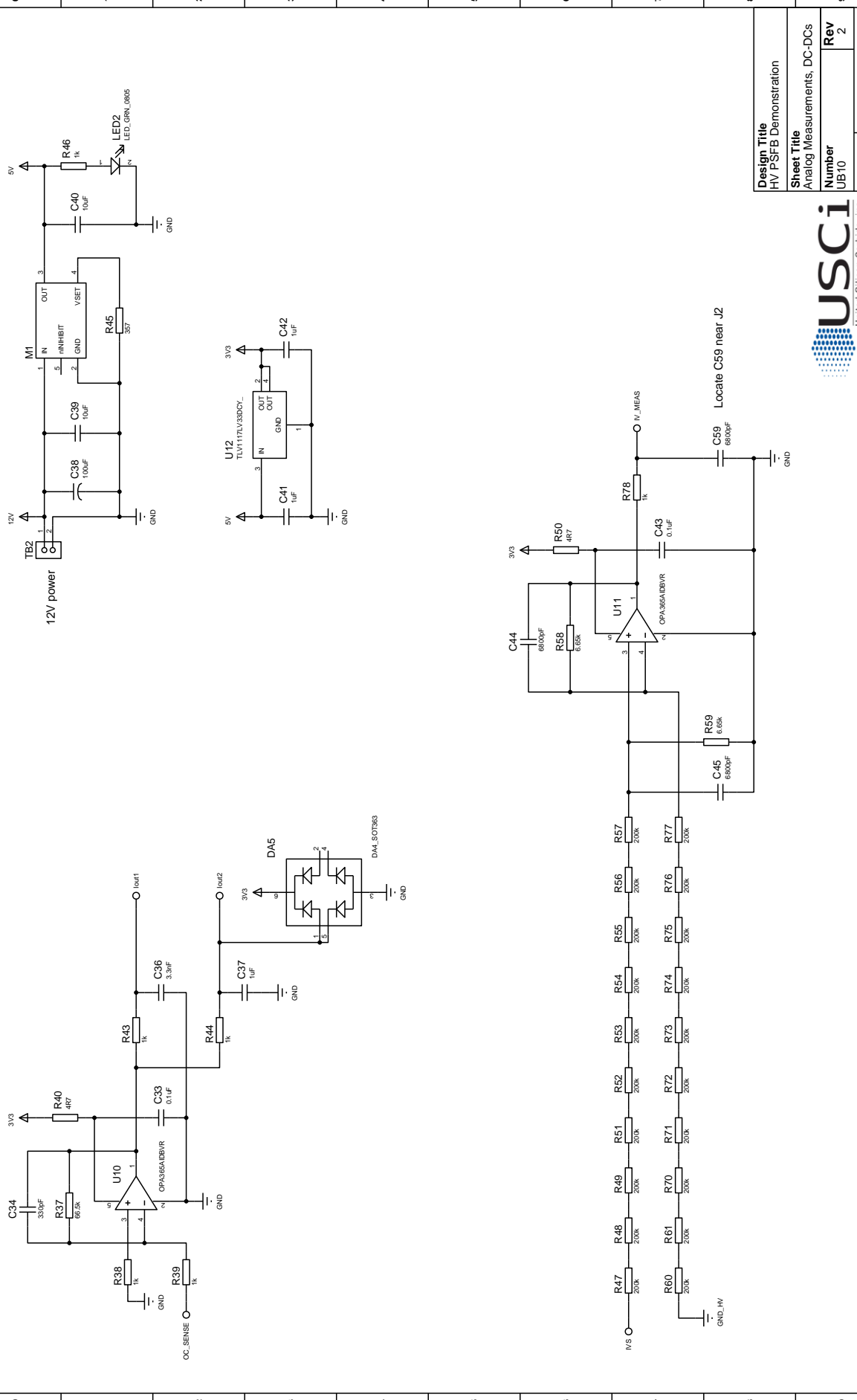
Design Title	HV PSFB Demonstration
Sheet Title	Processor Emulator
Number	UB10
Rev	2
Sheet B	Page: 1 of 3





Design Title	HV PSFB Demonstration
Sheet Title	Power Section
Number	UB10
Rev	2





Design Title	HV PSFB Demonstration
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Locate C59 near J2

Design Title	HV PSFB Demonstration
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Warnings and Safety Concerns

Always follow USCi's set-up and application instructions, including use of all interface components within their recommended electrical rated voltage and power limits. Always use electrical safety precautions to help ensure your personal safety and the safety of those working around you.

Contact USCi's Personnel if you have any questions ([732-355-0550](tel:732-355-0550))

Save all warnings and instructions for future reference.

Failure to follow warnings and instructions may result in personal injury, property damage, or death due to electrical shock and/or burn hazards.

USCi's PCB demo boards and gerber files are provided strictly for use in development laboratory environments, solely for qualified professional users having training, expertise, and knowledge of electrical safety risks in development and application of high-voltage electrical circuits.

Any other use and/or application are strictly prohibited by United Silicon Carbide.

If you are not suitably qualified, you should immediately stop.

Work Area Safety:

- (a) Keep work area clean and orderly.
- (b) Qualified observer(s) must be present anytime circuits are energized.
- (c) Effective barriers and signage must be present in the area where the USCi designs and products are energized, indicating operation of accessible high voltages may be present, for the purpose of protecting inadvertent access.
- (d) All interface circuits, power supplies, evaluation modules, instruments, meters, scopes and other related apparatus used in a development environment exceeding 50 V_{RMS}/75 VDC must be electrically located within a protected Emergency Power Off (EPO) protected power strip.
- (e) Use a stable and non-conductive work surface.
- (f) Use adequately insulated clamps and wires to attach measurement probes and instruments.
- (g) **No freehand testing.**

2. Electrical Safety:

- (a) De-energize the USCi Board and/or design and all inputs, outputs, and electrical loads before performing any electrical or other diagnostic measurements.
- (b) Confirm that USCi Board and/or design are de-energized before proceeding with any required electrical circuit configurations, wiring, measurement equipment hook-ups and other application needs.
- (c) Once USCi Board and/or design readiness is complete, energize the board/design as intended.

WARNING: while the USCi Board and/or design is energized, never touch it or its electrical circuits as they could be at high voltages capable of causing life threatening electrical shock.

3. Personal Safety:

- (a) Wear personal protective equipment e.g. latex gloves and/or safety glasses with side shields or protect USCi Board and/or design in an adequate lucent plastic box with interlocks from accidental touch.

4. Limitation for Safe Use:

- (a) USCi Boards and/or designs are not to be used as all or part of a production unit.