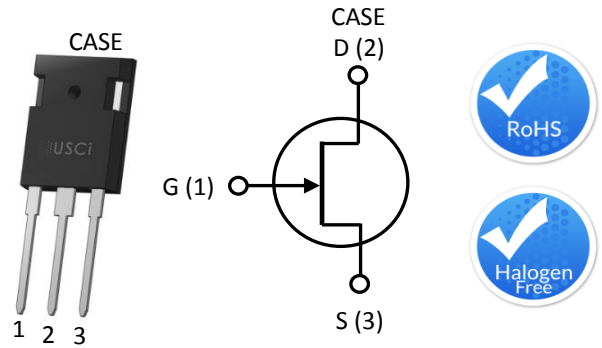


Description

United Silicon Carbide, Inc offers the xJ series of high-performance SiC normally-on JFET transistors. This series exhibits ultra-low on resistance ($R_{DS(ON)}$) and gate charge (Q_G) allowing for low conduction and switching loss. The device normally-on characteristics with low $R_{DS(ON)}$ at $V_{GS} = 0\text{ V}$ is also ideal for current protection circuits without the need for active control, as well as for cascode operation.



Part Number	Package	Marking
UJN1208K	TO-247-3L	UJN1208K

Features

- ◆ Low On-Resistance $R_{DS(on)max}$ of 0.080Ω
- ◆ Voltage controlled
- ◆ Maximum operating temperature of 175°C
- ◆ Extremely fast switching not dependent on temperature
- ◆ Low gate charge
- ◆ Low intrinsic capacitance
- ◆ RoHS compliant

Typical Applications

- ◆ Over Current Protection Circuits
- ◆ DC-AC Inverters
- ◆ Switch Mode Power Supplies
- ◆ Power Factor Correction Modules
- ◆ Motor Drives
- ◆ Induction Heating

Maximum Ratings

Parameter	Symbol	Test Conditions	Value	Units
Drain-source voltage	V_{DS}		1200	V
Gate-source voltage	V_{GS}	DC	-20 to +3	V
		AC ⁽¹⁾	-20 to +20	
Continuous drain current	I_D	$T_C = 25^\circ\text{C}$	21	A
		$T_C = 125^\circ\text{C}$	13	A
Pulsed drain current	I_{DM}	$T_j = 125^\circ\text{C}$	41	A
		$T_j = 175^\circ\text{C}$	35	
Power dissipation	P_{tot}	$T_C = 25^\circ\text{C}$	136	W
Operating and storage temperature	T_J, T_{STG}		-55 to 175	°C
Max lead temperature for soldering, 1/8" from Case for 5 Seconds	T_L		250	°C

(1) +20V AC rating applies for turn-on pulses <200ns applied with external $R_G > 1\Omega$.

Electrical Characteristics ($T_J = +25^\circ\text{C}$ unless otherwise specified)

Typical Performance - Static

Parameter	Symbol	Test Conditions	Value			Units
			Min	Typ	Max	
Drain-source breakdown voltage	BV_{DS}	$V_{GS} = -20V, I_D = 1mA$	1200			V
Total drain leakage current	I_D	$V_{DS} = 1200V,$ $V_{GS} = -20V, T_J = 25^\circ\text{C}$		40	500	μA
		$V_{DS} = 1200V,$ $V_{GS} = -20V, T_J = 175^\circ\text{C}$		120	1500	
Total gate leakage current	I_G	$V_{GS} = -20V, T_J = 25^\circ\text{C}$		0.3	125	μA
		$V_{GS} = -20V, T_J = 175^\circ\text{C}$		3		
Drain-source on-resistance	$R_{DS(on)}$	$V_{GS} = 2V, I_D = 10A,$ $T_J = 25^\circ\text{C}$		67	80	$\text{m}\Omega$
		$V_{GS} = 0V, I_D = 10A,$ $T_J = 25^\circ\text{C}$		77	95	
		$V_{GS} = 2V, I_D = 10A,$ $T_J = 175^\circ\text{C}$		200	240	
		$V_{GS} = 0V, I_D = 10A,$ $T_J = 175^\circ\text{C}$		230	285	
Gate threshold voltage	$V_{G(th)}$	$V_{DS} = 5V, I_D = 70mA$	-10	-7	-4	V
Gate Resistance	R_G	$V_{GS} = 0V, f = 1MHz$		6		Ω

Typical Performance - Dynamic

Parameter	symbol	Test Conditions	Value			Units	
			Min	Typ	Max		
Input capacitance	C_{iss}	$V_{DS} = 100V,$ $V_{GS} = -20V,$ $f = 100kHz$		500		pF	
Output capacitance	C_{oss}			94			
Reverse transfer capacitance	C_{rss}			93			
Effective output capacitance, energy related	$C_{oss(er)}$	$V_{DS} = 0V$ to 600V, $V_{GS} = -20V$		53		pF	
Total gate charge	Q_G	$V_{DS}=600V, I_D = 20A,$ $V_{GS}=-15V$ to 2.5V		62		nC	
Gate-drain charge	Q_{GD}			44			
Gate-source charge	Q_{GS}			6			
Turn-on delay time	$t_{d(on)}$	$V_{DS}=600V, I_D=20A,$ Gate Driver = -15V to +5V, $R_{G,EXT} = 2.5\Omega,$ Inductive Load, $T_J = 25^\circ C$		11		ns	
Rise time	t_r			30			
Turn-off delay time	$t_{d(off)}$			23			
Fall time	t_f			26			
Turn-on energy	E_{ON}				202		μJ
Turn-off energy	E_{OFF}				210		
Total switching energy	E_{TOTAL}				412		
Turn-on delay time	$t_{d(on)}$		$V_{DS}=600V, I_D=20A,$ Gate Driver = -15V to +5V, $R_{G,EXT} = 2.5\Omega,$ Inductive Load, $T_J = 150^\circ C$		11		ns
Rise time	t_r				33		
Turn-off delay time	$t_{d(off)}$				22		
Fall time	t_f			23			
Turn-on energy	E_{ON}				220		μJ
Turn-off energy	E_{OFF}				174		
Total switching energy	E_{TOTAL}				394		

Thermal Characteristics

Parameter	symbol	Test Conditions	Value			Units
			Min	Typ	Max	
Thermal resistance, junction-to-case	$R_{\theta JC}$				1.1	$^\circ C/W$

Typical Performance Diagrams

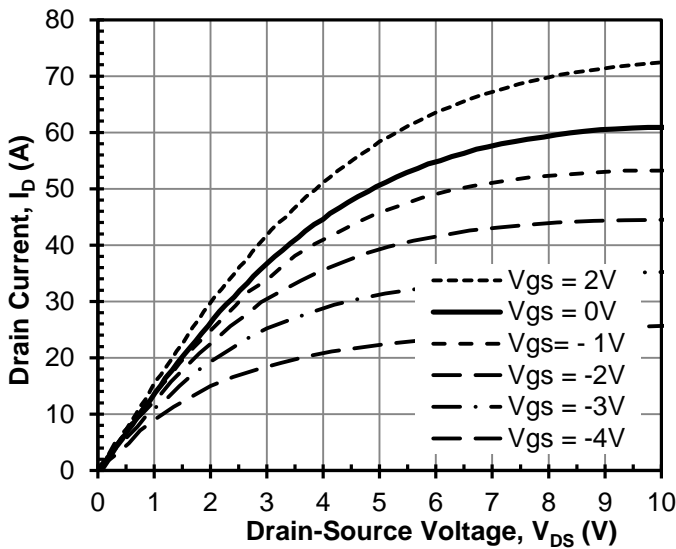


Figure 1 Typical output characteristics at $T_j = 25^\circ\text{C}$

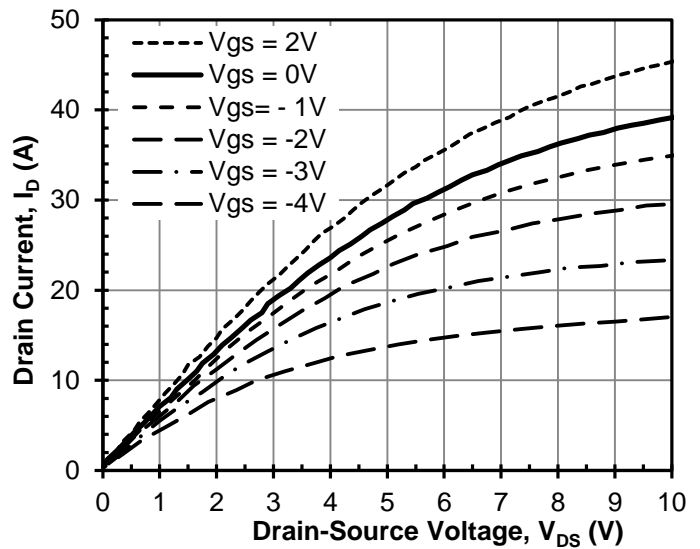


Figure 2 Typical output characteristics at $T_j = 125^\circ\text{C}$

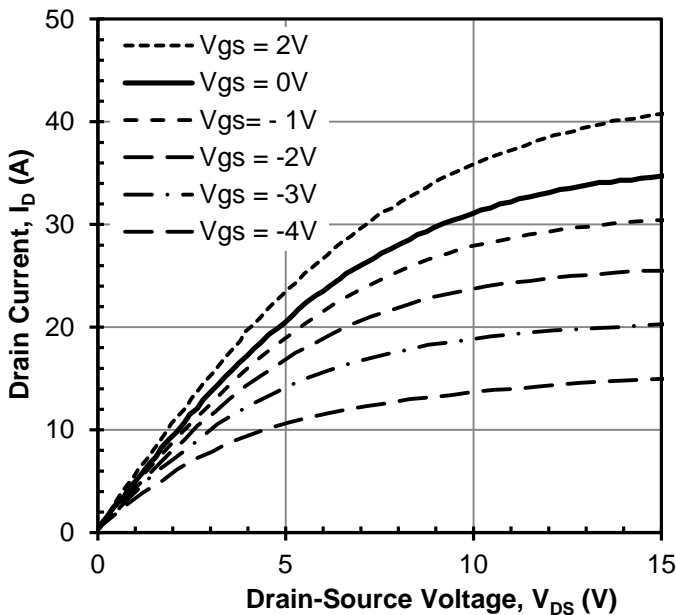


Figure 3 Typical output characteristics at $T_j = 175^\circ\text{C}$

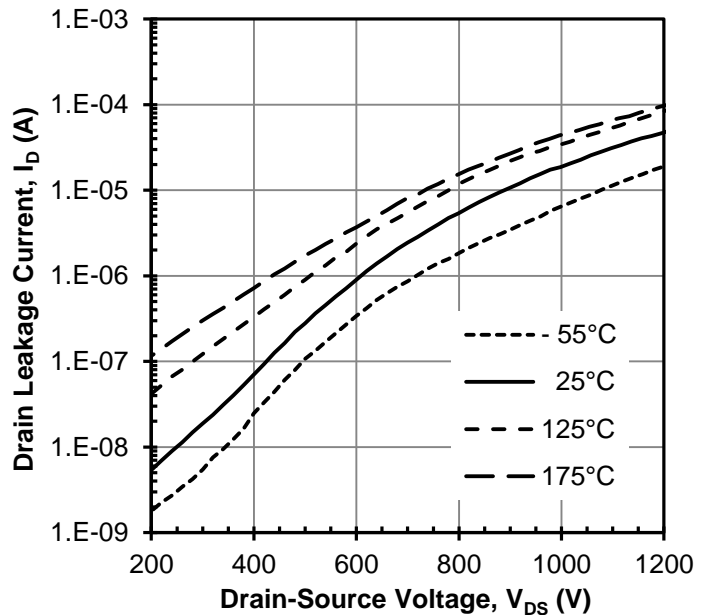


Figure 4 Typical drain-source leakage at $V_{GS} = -20\text{V}$

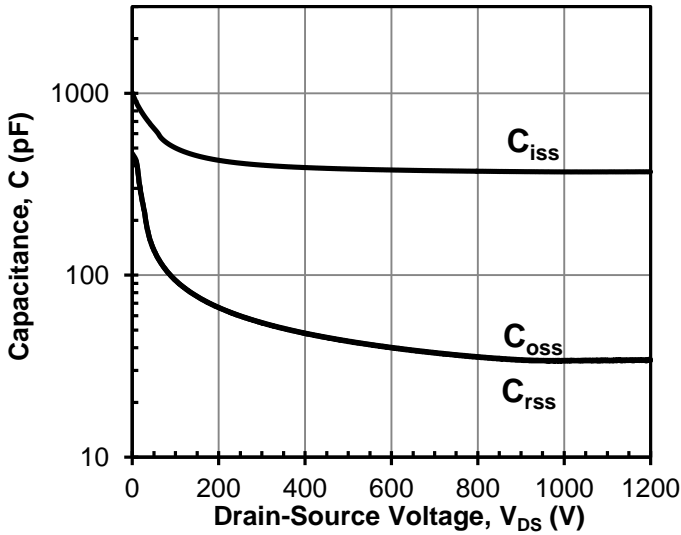


Figure 5 Typical capacitances at 100kHz and $V_{GS} = -20V$

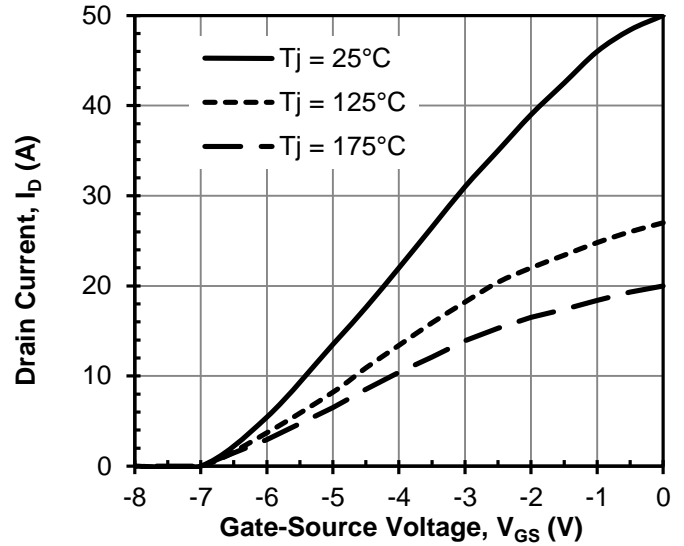


Figure 6 Typical transfer characteristics at $V_{DS} = 5V$

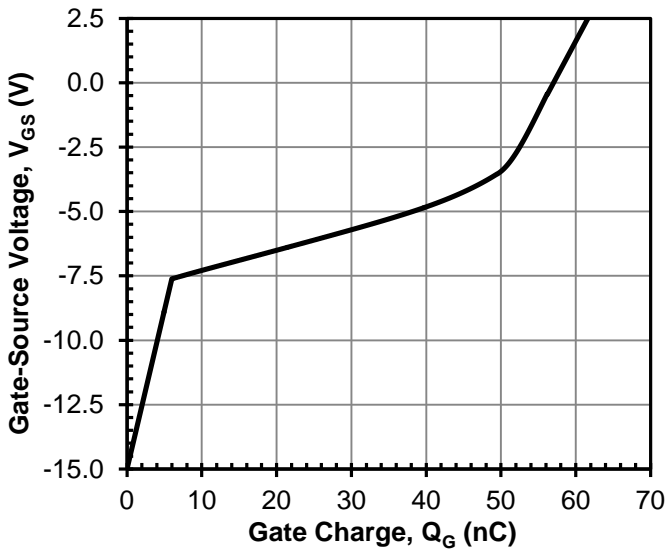


Figure 7 Typical gate charge at $V_{DS} = 600V$ and $I_D = 20A$

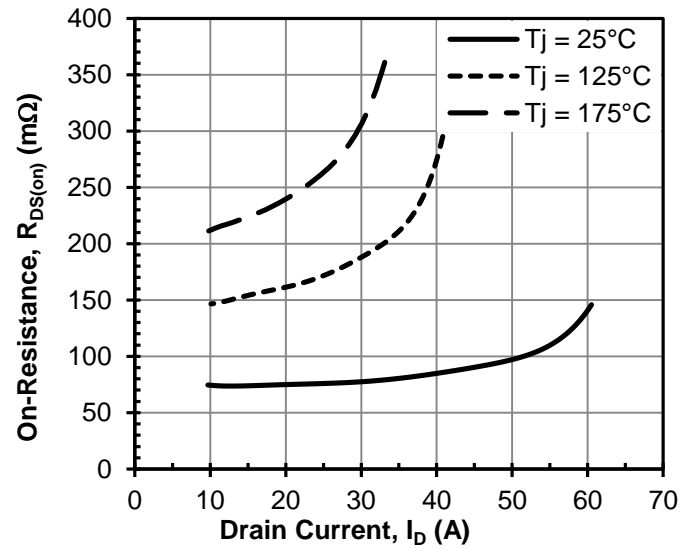


Figure 8 Typical drain-source on-resistance at $V_{GS} = 0V$

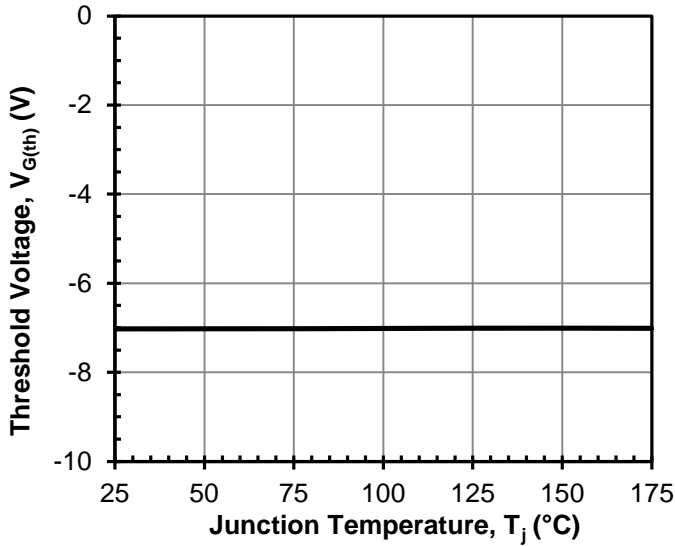


Figure 9 Threshold voltage vs. T_j
at $V_{DS} = 5V$ and $I_D = 70mA$

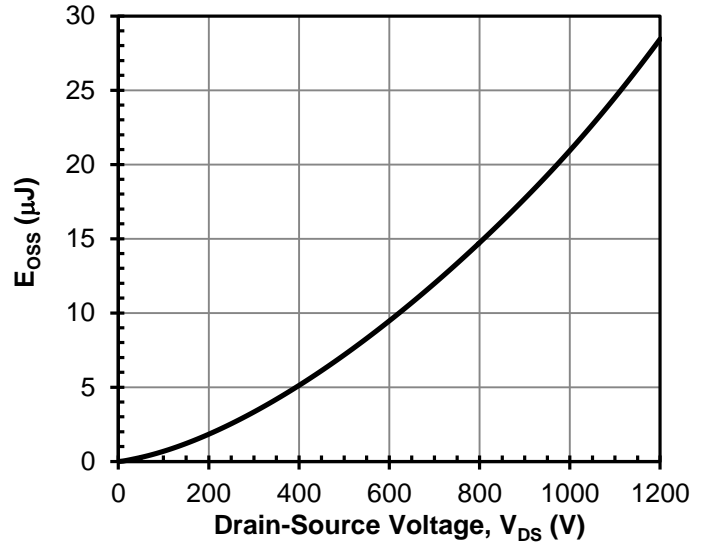


Figure 10 Typical stored energy in C_{oss}
at $V_{GS} = -20V$

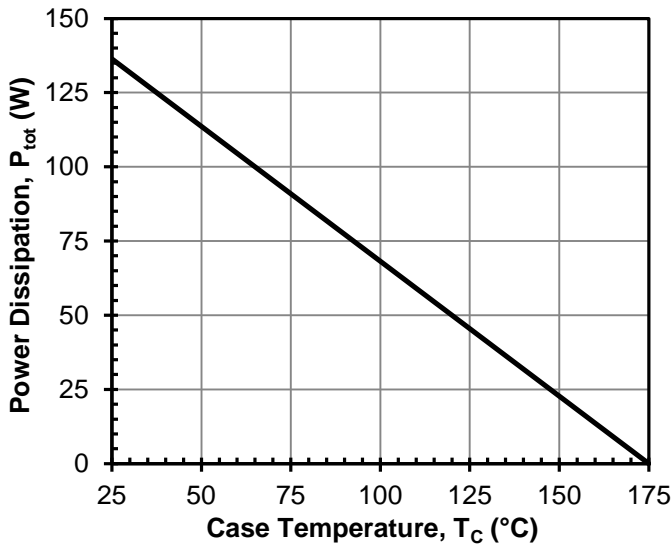


Figure 11 Total power Dissipation

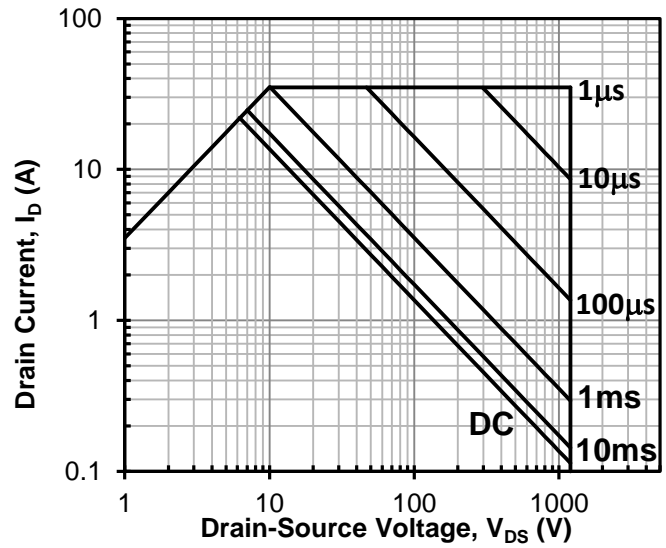


Figure 12 Safe operation area
 $T_c = 25^\circ C$, Parameter t_p

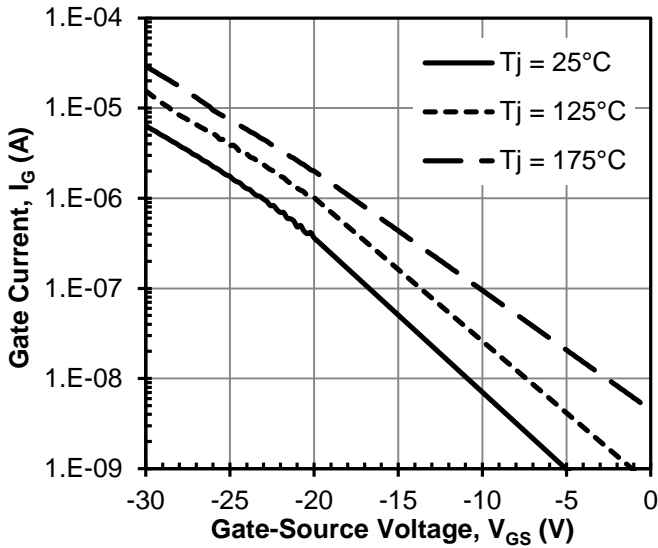


Figure 13 Typical gate leakage current at $V_{DS} = 0V$

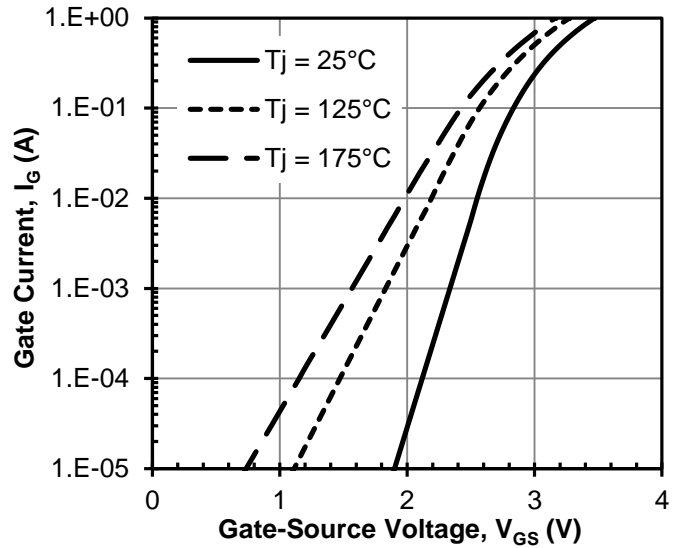


Figure 14 Typical gate forward current at $V_{DS} = 0V$

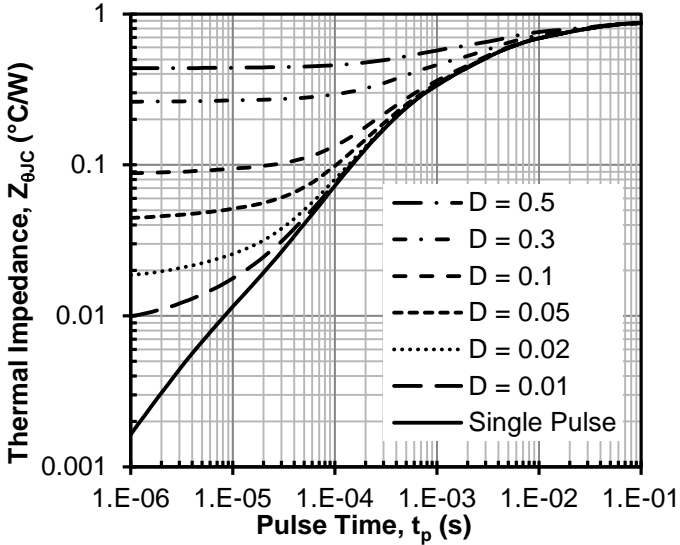


Figure 15 Maximum transient thermal impedance

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